

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In application of: RAMINDA U. MADURawe
Application No.:
Title: Integrated Circuits with RAM and ROM Fabrication Options
Filed: 12/31/2003

INFORMATION DISCLOSURE STATEMENT SUBMISSION

Sirs:

Attached is IDS form 1449 along with copies of the references for the Application submitted herewith entitled "Integrated Circuits with RAM and ROM Fabrication Options".

Regards,


Raminda U. Madurawe

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to:

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By:


Raminda U. Madurawe

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PTO/SB/08A (08-00)

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Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>			Complete if Known		
			Application Number		
			Filing Date	12/31/2003	
			First Named Inventor	Madurawe, Raminda U.	
			Group Art Unit		
			Examiner Name		
			Attorney Docket Number		
Sheet	1	of	2		

U.S. PATENT DOCUMENTS						
Examiner Initials *	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
		4,609,986		Hartmann et al.	09-02-1986	
		4,706,216		Carter	11-10-1987	
		4,761,768		Turner et al.	08-02-1988	
		4,857,774		El-Ayat et al.	08-15-1989	
		4,870,302		Freeman	09-26-1989	
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		2003/0085733		Pugh et al.	05-08-2003	

FOREIGN PATENT DOCUMENTS								
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		Office ³	Number ⁴	Kind Code ⁵ (if known)				
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Filing Date		12/31/2003			
First Named Inventor		Madurawe, Raminda U.			
Group Art Unit					
Examiner Name					
Attorney Docket Number					
Sheet	2	of	2		

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		SEALS & WHAPSHOTT, "Programmable Logic – PLDs and FPGAs", 1997, pp 102-117, McGraw-Hill, USA	<input type="checkbox"/>
		ASHOK K. SHARMA, "Programmable Logic Handbook – PLDs, CPLDs, & FPGAs", 1998, pp 99-171, McGraw-Hill, USA	<input type="checkbox"/>
		S. M. SZE, "VLSI Technology", 1983, pp 600-609, McGraw-Hill, New York, USA	<input type="checkbox"/>
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